



Webinar Invitation

Introduction to FPGA design with Efinix

Thursday, 22.2.2024 [9 a.m. to 10:30 a.m.] – online

With the TEC BYTES webinars, [TRS-STAR](#) offers you bite-sized technical tidbits. [TRS-STAR](#) customers are cordially invited to this free training opportunity. **In addition, you have the opportunity to receive a free T8 Board:** we will determine the three winners of this board among the first 10 registrations.

Efinix FPGAs use the disruptive **Quantum™** technology for their low-power, cost-effective, compact and resource-conserving FPGAs. The FPGAs scale from 4 kLUT to currently 375 kLUT and also offer SoC variants with a RISC-V Quad-Core Processor [up to 1 GHz].

In the webinar, Andreas Schwarztrauber (CEO of [TRS-STAR](#)) will give you a brief overview of the Efinix FPGA families. Afterwards, FH-Prof. DI Dr. Markus Pfaff (CEO & Co-Founder [P2L2](#)) will show you how to easily get started with or switch to this fascinating FPGA technology.

Speaker: Prof. DI Dr. Markus Pfaff (CEO & Co-Founder [P2L2](#))
Andreas Schwarztrauber (CEO [TRS-STAR](#))

Language: English

Prerequisites technical understanding, basic knowledge of VHDL desirable but not required. [T8 Board](#) for customers who want to reproduce the hands-on afterwards.

Seminar form: Presentation, hands-on at your own time and space

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Agenda [time zone: CET]

- 09:00 am - 09:05 am Welcome and introduction [Andreas Schwarztrauber]
- 09:05 am - 09:15 am Efinix Quantum™ technology [Andreas Schwarztrauber]
- 09:15 am - 09:30 am Overview of the Efinix FPGA families [Andreas Schwarztrauber]
- 09:30 am - 09:40 am Special features of the Efinity tool flow [FH-Prof. DI Dr. Markus Pfaff]
- 09:40 am - 10:20 am Introduction to the Efinity Toolflow [FH-Prof. DI Dr. Markus Pfaff]
- 10:20 am - 10:30 am Questions and answers

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