



## Efinix® – disruptive FPGA innovation presented by TRS-STAR

Dear valued customer Leonis (EN),

Efinity® IDE v2023.1 (released on June 26th 2023) has been assessed by exida per the relevant requirements of IEC 61508:2010 and ISO 26262:2018.

Efinity® IDE v2023.1 meets all requirements providing a level of integrity to achieve SIL-4 or ASIL-D.



The certificate and assessment report can be found on the exida SAEL (Safety Automation Equipment List). The SAEL is a regularly maintained list of instrumentation that is functional safety certified per exida standards for use in safety instrumented systems.

### Replace your old CPLD with a modern FPGA



The T13/T20Q100F3 SiP (System in Package) is now in full production. The SiP contains non-volatile memory (16 Mbit SPI-Flash), so the Power-Up behaviour is similar to a CPLD. The 100 pin LQFP 14 mm x 14 mm package with 0,5 mm pitch also reminds of a CPLD, but at heart of the SiP is a modern Trion® T13 or T20 FPGA. Of course a CPLD is instant-on, whereas an FPGA has short boot-time. E.g. a T13/20 has a typical boot time of a few ms, but for many applications this is acceptable.

Send us a message to [fpga@trs-star.com](mailto:fpga@trs-star.com), to get an attractive price for production or if you need help converting your old CPLD to a modern FPGA. We are happy to assist.



[Efinix® Trion® FPGA Overview](#)

### New Ti180 packaging variants

The Ti180 is now available in many packaging variants (J361, G400, J484, G529) scaling from 13 mm x 13 mm to 19 mm x 19 mm.

G400 and J484 are available in **AEC-Q 100 Grade 2**.

Size BxTxH [mm]	HVIO	HSIO	MIPI	DDR4/4x [Gbps]
<b>J361   Smallest Size</b>				
13 x 13 x 0,65	20	110	2 ports	48 (x16)
<b>G400   I/O Rich</b>				
16 x 16 x 0,8 mm	74	200	0	0
<b>J484 &amp; L484   Rich Hardcore IP</b>				
18 x 18 x 0,8 mm	27	116	4 ports	96 (x32)
<b>G529   High Performance</b>				
19 x 19 x 0,8 mm	48	210	0	106.6 (x32)



[Efinix® Titanium FPGA Overview](#)

### TRS-STAR Tec Bytes is now available on YouTube.

TRS-STAR presents bite-sized presentations and information on interesting technological topics with a focus on – but not limited to - FPGAs, power and displays. Some tec b(y)tes are smaller, some are bigger, but they are hopefully always interesting. No registration required.



### New to Efinix?

Contact [fpga@trs-star.com](mailto:fpga@trs-star.com) for design software, online trainings, eval boards, samples or anything else you need to start your FPGA design today.

#### More informations:

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